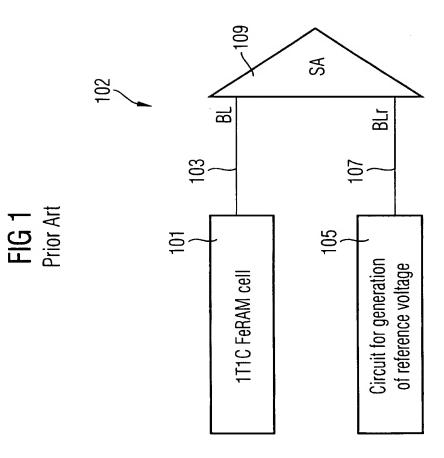
Appln No.: 10/665,401 Page 1 of 7
Applicant(s): Michael Jacob et al.
REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE
DEGRADATION OF REFERENCE CAPACITORS PROVIDING
REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES



1/7



Page 2 of 7

Appln No.: 10/665,401 Page 2 of 7
Applicant(s): Michael Jacob et al.
REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE
DEGRADATION OF REFERENCE CAPACITORS PROVIDING
REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES

2/7

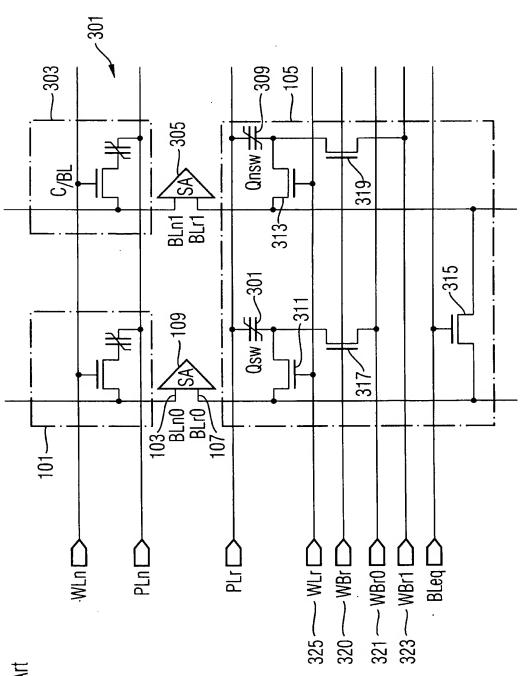
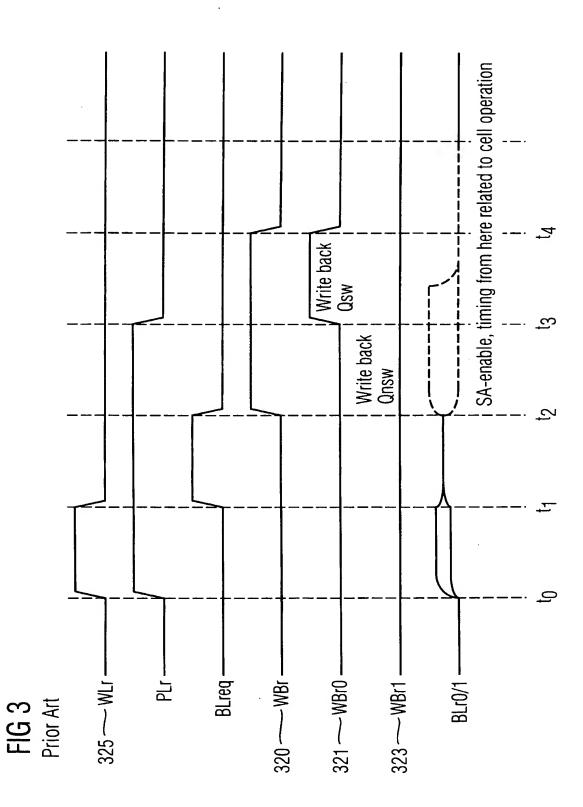


FIG 2 Prior Art

Appln No.: 10/665,401 Applicant(s): Michael Jacob et al.

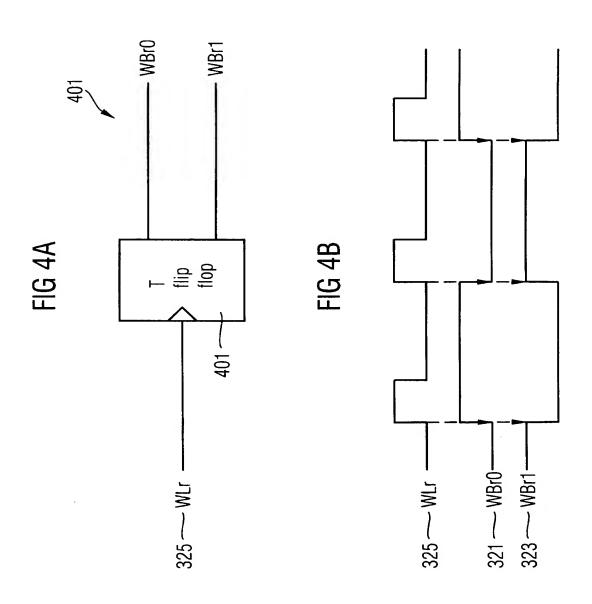
REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE DEGRADATION OF REFERENCE CAPACITORS PROVIDING REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES

3/7



Appln No.: 10/665,401 Page 4 of 7
Applicant(s): Michael Jacob et al.
REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE
DEGRADATION OF REFERENCE CAPACITORS PROVIDING
REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES

4/7



Appln No.: 10/665,401 Applicant(s): Michael Jacob et al.

REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE DEGRADATION OF REFERENCE CAPACITORS PROVIDING REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES

5/7

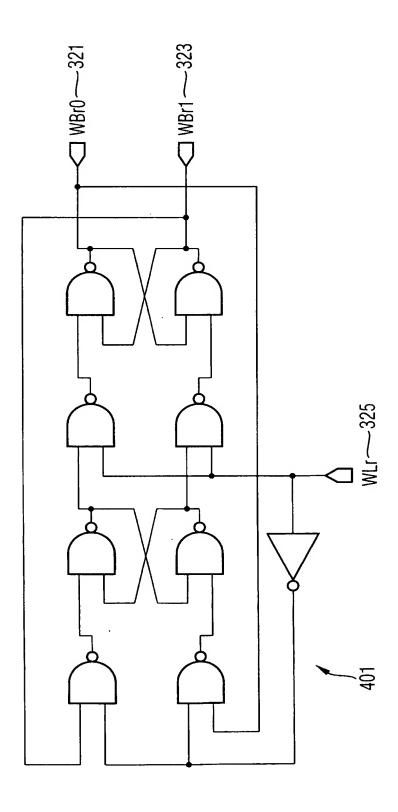


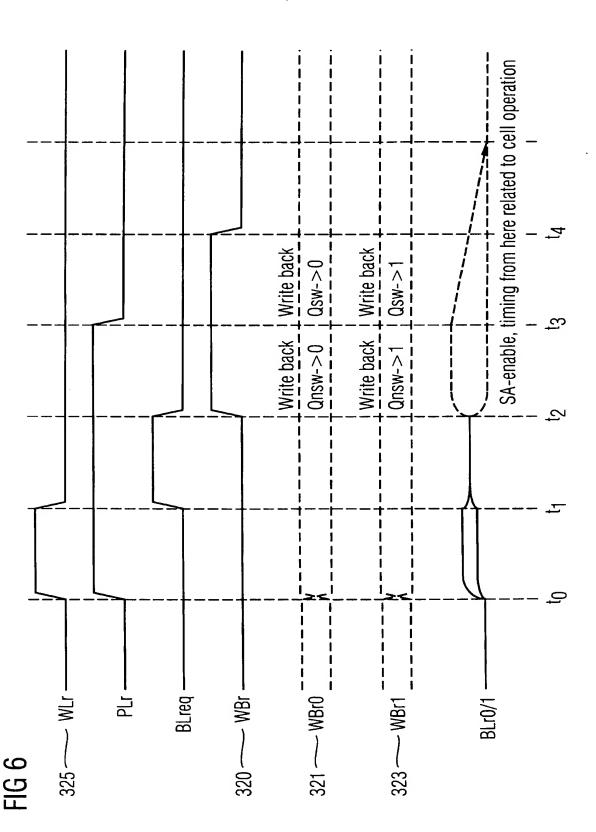
FIG 5

Page 6 of 7

Appln No.: 10/665,401 Applicant(s): Michael Jacob et al.

REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE DEGRADATION OF REFERENCE CAPACITORS PROVIDING REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES





Appln No.: 10/665,401 Page 7 of 7
Applicant(s): Michael Jacob et al.
REFERENCE CIRCUIT IMPLEMENTED TO REDUCE THE
DEGRADATION OF REFERENCE CAPACITORS PROVIDING
REFERENCE VOLTAGES FOR 1T1C FERAM DEVICES

7/7

